

Название опции	Значение	Название опции	Значение
Ai Overclock Tuner	[Manual]	DRAM Dynamic Write Control	[Auto]
Ratio CMOS Setting	[6]	Меню DRAM Skew Control	
FSB Strap to North Bridge	[Auto]	DRAM CMD Skew on Chanel A	[Auto]
FSB Frequency	[533]	DRAM CLK Skew on DIMM A1	[Auto]
PCIE Frequency	[100]	DRAM CLK Skew on DIMM A2	[Auto]
DRAM Frequency	[DDR3-2133]	DRAM CTL Skew on DIMM A1	[Auto]
DRAM Command Rate	[1N]	DRAM CTL Skew on DIMM A2	[Auto]
DRAM Timing Control	[Manual]	DRAM CMD Skew on Chanel B	[Advance 150ps]
CAS# Latency	[9 DRAM Clocks]	DRAM CLK Skew on DIMM B1	[Auto]
RAS# to CAS# Delay	[9 DRAM Clocks]	DRAM CLK Skew on DIMM B2	[Advance 250ps]
RAS# Precharge	[9 DRAM Clocks]	DRAM CTL Skew on DIMM B1	[Auto]
RAS# Active Time	[24 DRAM Clocks]	DRAM CTL Skew on DIMM B2	[Auto]
RAS# to RAS# Delay	[Auto]	Конец Меню DRAM Skew Control	
Row Refresh Cycle Time	[Auto]	Ai Clock Twister	[Auto]
Write Recovery Time	[Auto]	Ai Transaction Booster	[Auto]
Read to Precharge Time	[Auto]	CPU Voltage	[1.35000]
Read to Write Delay (S/D)	[10 DRAM Clocks]	CPU GTL Voltage Reference(0/2)	[Auto]
Write to Read Delay (S)	[6 DRAM Clocks]	CPU GTL Voltage Reference(1/3)	[Auto]
Write to Read Delay (D)	[5 DRAM Clocks]	CPU PLL Voltage	[Auto]
Read to Read Delay (S)	[5 DRAM Clocks]	FSB Termination Voltage	[Auto]
Read to Read Delay (D)	[7 DRAM Clocks]	DRAM Voltage	[2.00v]
Write to Write Delay (S)	[5 DRAM Clocks]	NB Voltage	[Auto]
Write to Write Delay (D)	[7 DRAM Clocks]	NB GTL Voltage Reference	[0.61]
Write to PRE Delay	[24 DRAM Clocks]	SB Voltage	[Auto]
Read to PRE Delay	[8 DRAM Clocks]	Clock Over-Charging Voltage	[Auto]
PRE to PRE Delay	[1 DRAM Clocks]	Load-Line Calibration	[Auto]
ALL PRE to ACT Delay	[10 DRAM Clocks]	CPU Spread Spectrum	[Auto]
ALL PRE to REF Delay	[10 DRAM Clocks]	PCIE Spread Spectrum	[Auto]